

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Appln. of: Hichem Abdallah ET AL.
 Appln. No.: To Be Assigned
 Filed: Herewith
 For: SEMICONDUCTOR
 COMPONENT WITH
 INTEGRATED CAPACITANCE
 STRUCTURE HAVING A
 PLURALITY OF
 METALLIZATION PLANES

Examiner: To Be Assigned
 Art Unit: To Be Assigned

Attorney Docket No: 10808/170

INFORMATION DISCLOSURE STATEMENT

In accordance with the duty of disclosure under 37 C.F.R. §1.56 and §§1.97-1.98, and more particularly in accordance with 37 C.F.R. §1.97(b), Applicants hereby cite the following reference(s):

No.	Date of Publication	Patentee/Applicant/Assignee
5,208,725	May 4, 2003	Akcasu
5,583,359	December 10, 1996	Ng et al.
6,100,591	August 8, 2000	Ishii
6,298,500 B1	March 27, 2001	Alexander et al.
Foreign Patent Documents		
No.	Date of Publication	Country
EP 1109227A2	June 20, 2001	EPO
DE 100 19 839 A1	June 21, 2001	Germany
WO 01/99163 A2	December 27, 2001	WIPO
Other Art		
"Capacity limits and matching properties of integrated capacitors," by R. Aparicio et al., IEEE JOURNAL OF SOLID-STATE CIRCUITS, IEEE INC. New York, US, vol. 37, no. 3, March 2002, pages 384-393.		
"Capacity limits and matching properties of lateral flux integrated capacitors," by H. Aparicio et al., IEEE Custom Integrated Circuits Conference, San Diego, May 6-9, 2001, pages 365-368.		

10/511189
DT04 Rec'd PCT/PTO 12 OCT 2004

For the Examiner's convenience, Applicants are enclosing Form PTO-1449 (one sheet), along with a copy of each listed reference for which a copy is required under 37 C.F.R.

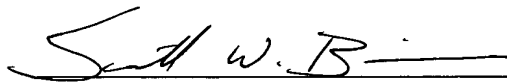
§1.98(a)(2)(as modified by the Commissioner's Notices dated July 11, 2003, and September 21, 2004). Listed on Form PTO-1449 is German Reference DE 100 19 839 A1, cited during prosecution of German Application Serial No. 102 17 556.7 filed April 19, 2002, which the current application claims priority to. Applicants respectfully request the Examiner's consideration of the above reference(s) and entry thereof into the record of this application.

By submitting this Statement, Applicants are attempting to fully comply with the duty of candor and good faith mandated by 37 C.F.R. §1.56. As such, this Statement is not intended to constitute an admission that any of the enclosed references, or other information referred to therein, constitutes "prior art" or is otherwise "material to patentability," as that phrase is defined in 37 C.F.R. §1.56(a).

Applicants have calculated no fee to be due in connection with the filing of this Statement. However, the Director is authorized to charge any fee deficiency associated with the filing of this Statement to a deposit account, as authorized in the Transmittal accompanying this Statement.

Respectfully submitted,

10/12/04
Date


Scott W. Brim (Reg. No. 51,500)

FORM PTO-1449	SERIAL NO. To Be Assigned	CASE NO. 107511189
LIST OF PATENTS AND PUBLICATIONS FOR APPLICANT'S INFORMATION DISCLOSURE STATEMENT	FILING DATE Herewith	GROUP ART UNIT To Be Assigned
(use several sheets if necessary)	APPLICANT(S): Hichem Abdallah et al.	

REFERENCE DESIGNATION U.S. PATENT DOCUMENTS

EXAMINER INITIAL		DOCUMENT NUMBER <small>Number-Kind Code (if known)</small>	DATE	NAME	CLASS/ SUBCLASS	FILING DATE
	A1	5,208,725	5/4/03	Akcasu		
	A2	5,583,359	12/10/96	Ng et al.		
	A3	6,100,591	8/8/00	Ishii		
	A4	6,298,500 B1	3/27/01	Alexander et al.		

FOREIGN PATENT DOCUMENTS

EXAMINER INITIAL		DOCUMENT NUMBER <small>Number-Kind Code (if known)</small>	DATE	COUNTRY	CLASS/ SUBCLASS	TRANSLATION YES OR NO
	A5	EP 1109227A2	6/20/01	EPO		
	A6	DE 100 19 839 A1	6/21/01	Germany		
	A7	WO 01/99163 A2	12/27/01	WIPO		

EXAMINER INITIAL	OTHER ART – NON PATENT LITERATURE DOCUMENTS <small>(Include name of author, title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date page(s), volume-issue number(s), publisher, city and/or country where published.)</small>					
	A8	"Capacity limits and matching properties of integrated capacitors," by R. Aparicio et al., IEEE JOURNAL OF SOLID-STATE CIRCUITS, IEEE INC. New York, US, vol. 37, no. 3, March 2002, pages 384-393.				
	A9	"Capacity limits and matching properties of lateral flux integrated capacitors," by H. Aparicio et al., IEEE Custom Integrated Circuits Conference, San Diego, May 6-9, 2001, pages 365-368.				

EXAMINER	DATE CONSIDERED
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EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609; Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.